



Application of High-Thermoelectric-Power Materials to Self-Cooling Device

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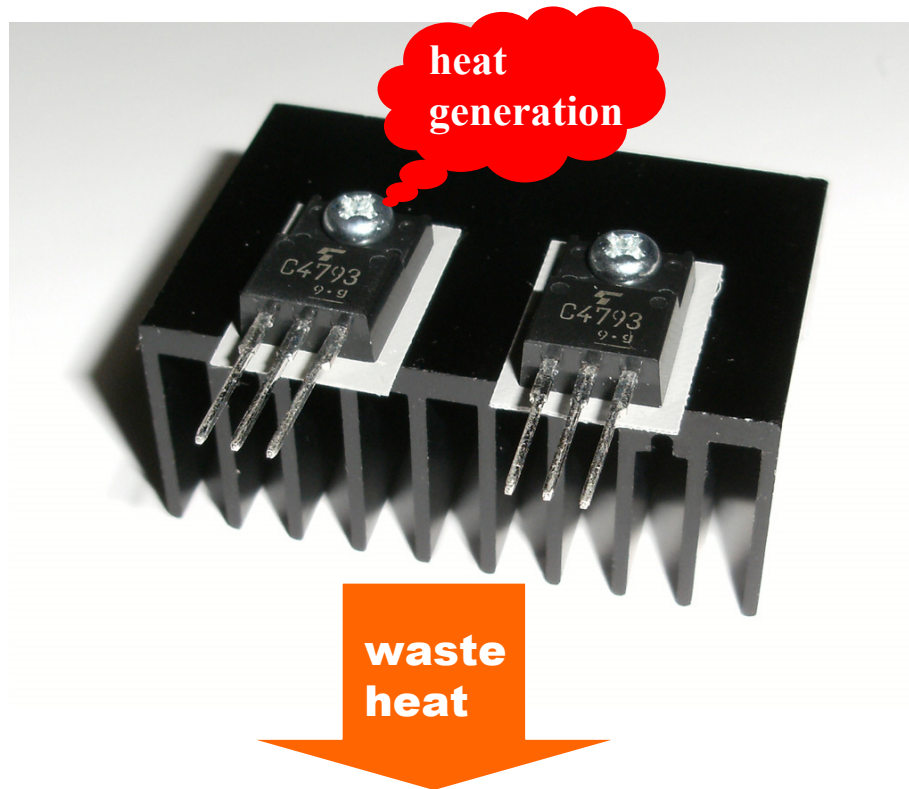
¹Yokohama National University,

²National Defence Academy,

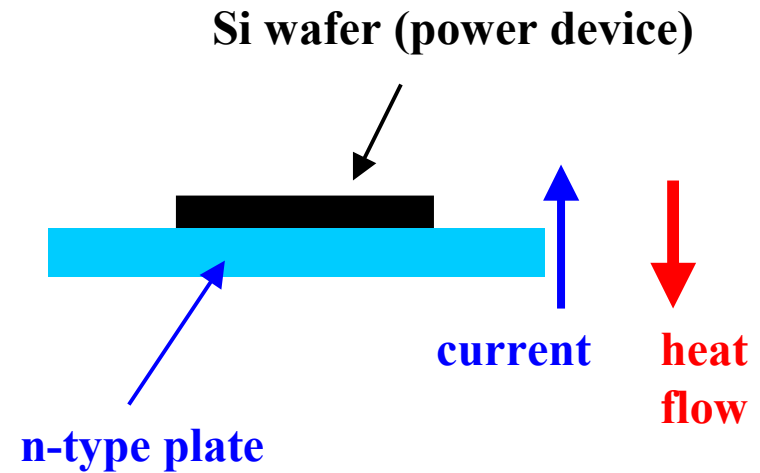
³Chubu University

The 3rd International Congress on Ceramics (ICC3) Symposium 9C: Ceramics for Electricity; Direct Conversion Technology between Heat and Electricity, 14-18 November 2010 in Osaka

Introduction



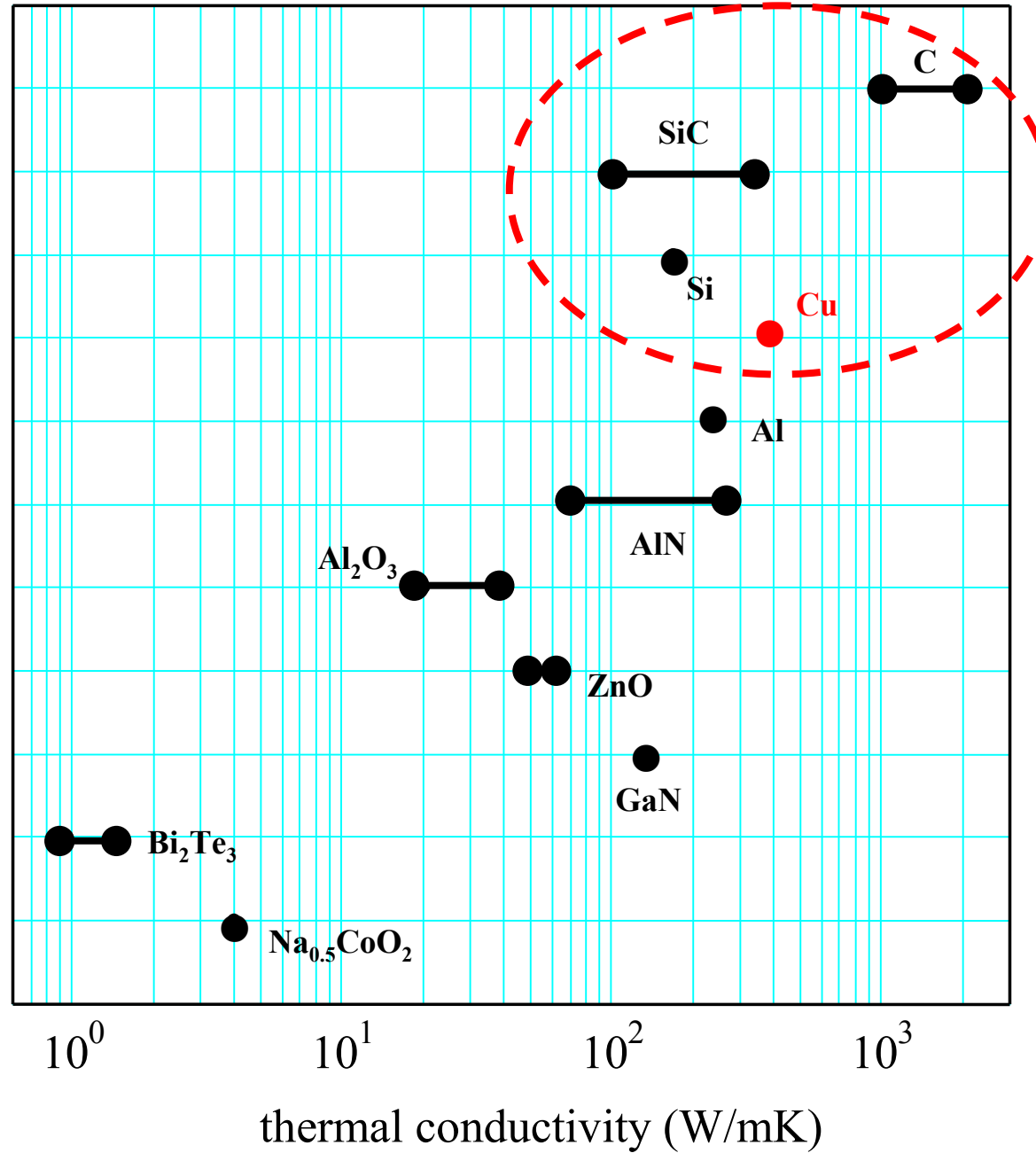
heat spread by heatsink
heat release to air by fin or fan



Uses heat flow by both thermal conduction and by Peltier heat for its electric current.

self-cooling device

S.Yamaguchi, ULVAC, 52, 14 (2007)

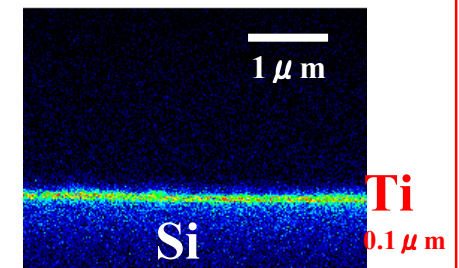
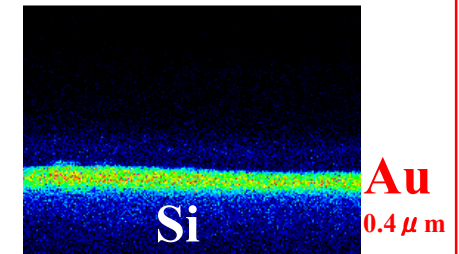
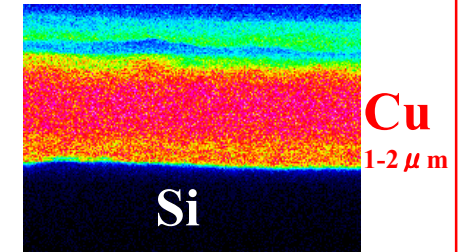
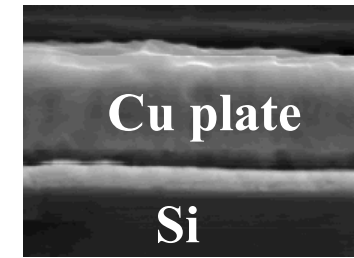


Experimental

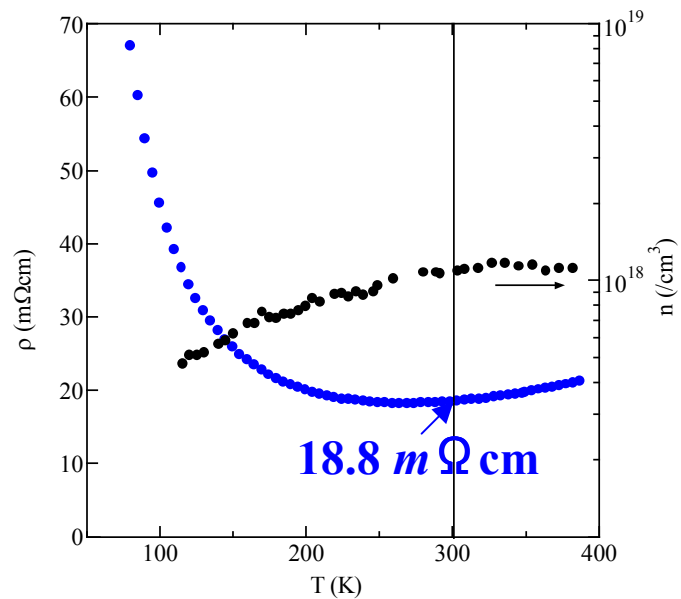
Electron Probe Micro Analyzer (EPMA)



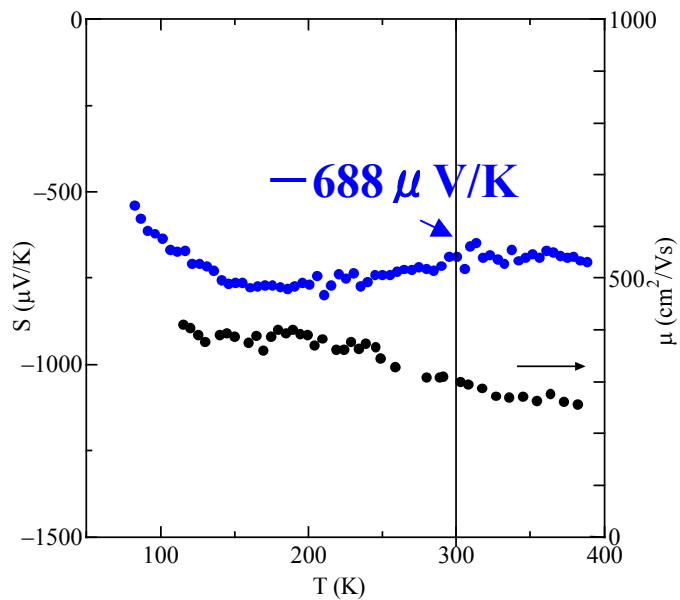
copper plating n-type Si (111) wafer

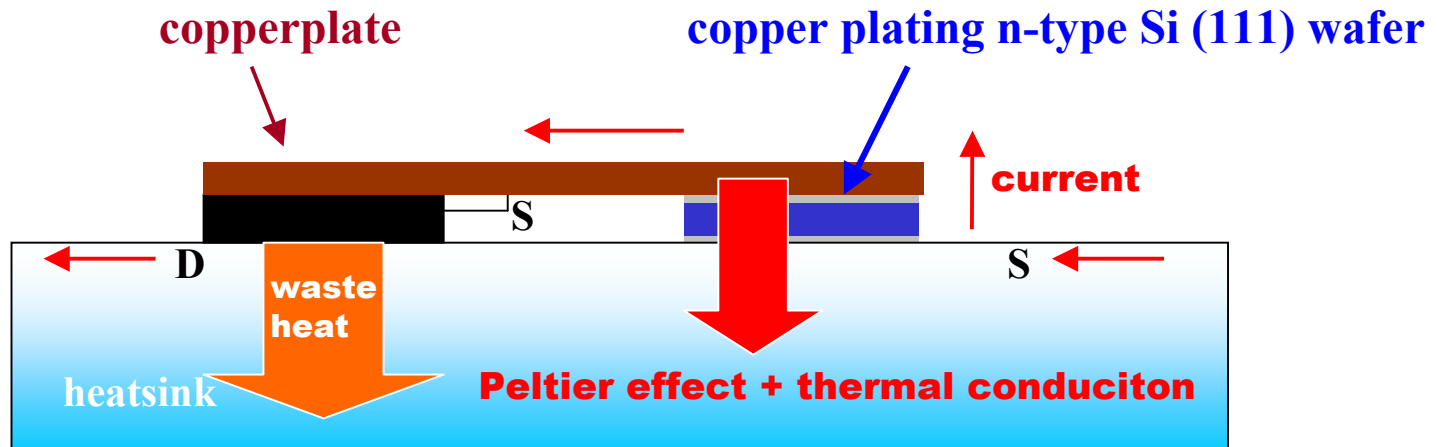
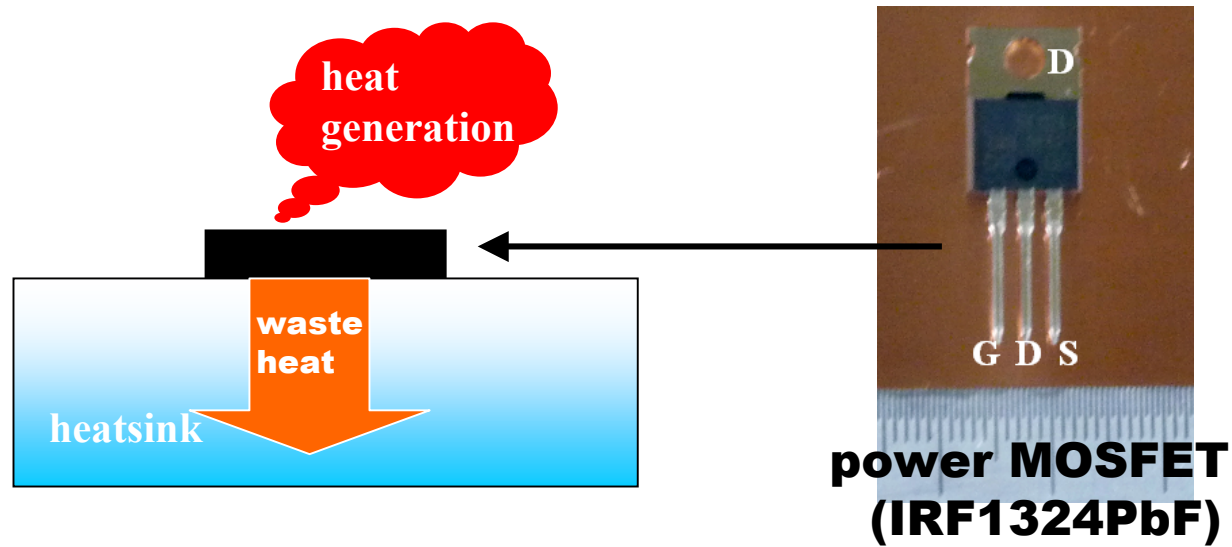


electrical resistivity



Seebeck coefficient





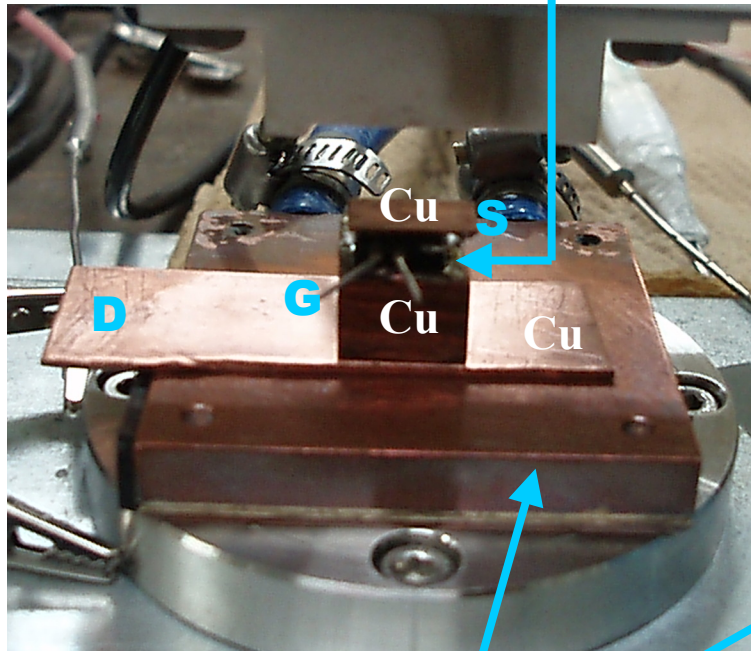
Schematic structure of self-cooling device



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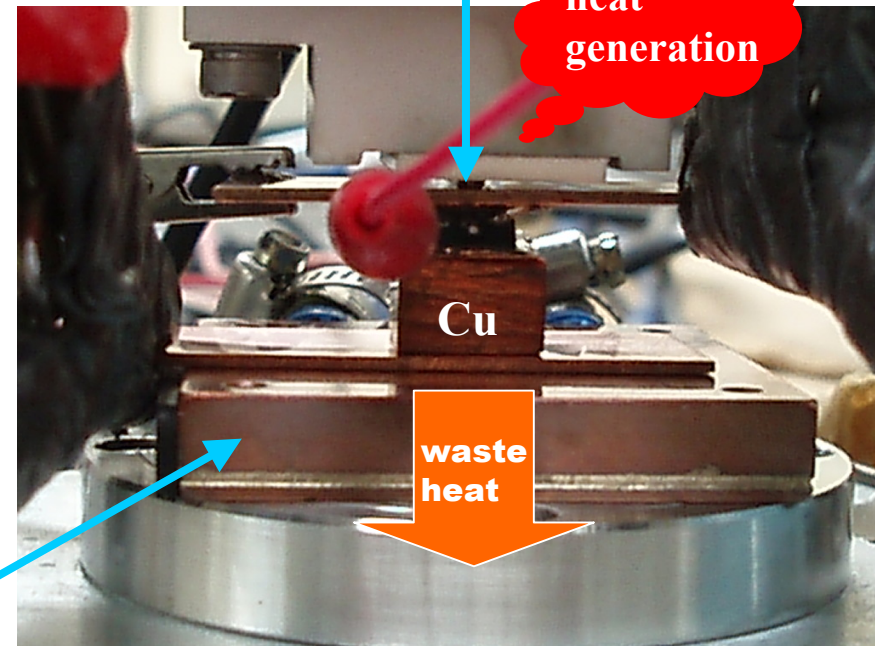
Results & Discussion

power MOSFET (IRF1324PbF)



water cooled heatsink ($10 \pm 2^\circ\text{C}$)

thermocouple



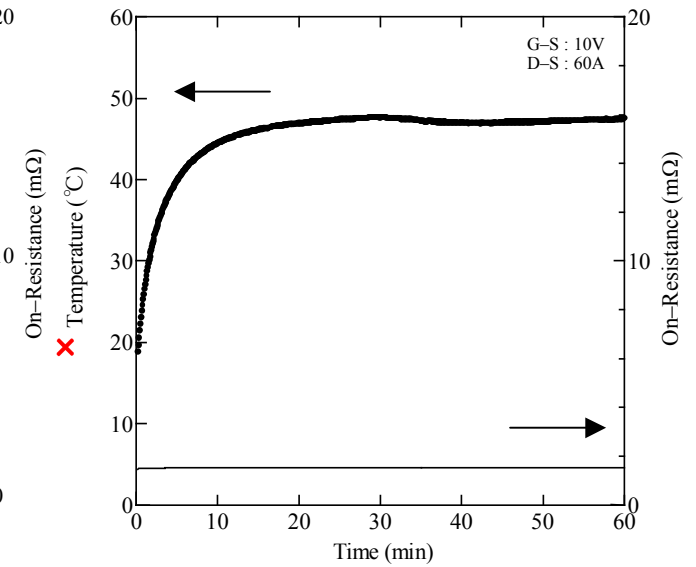
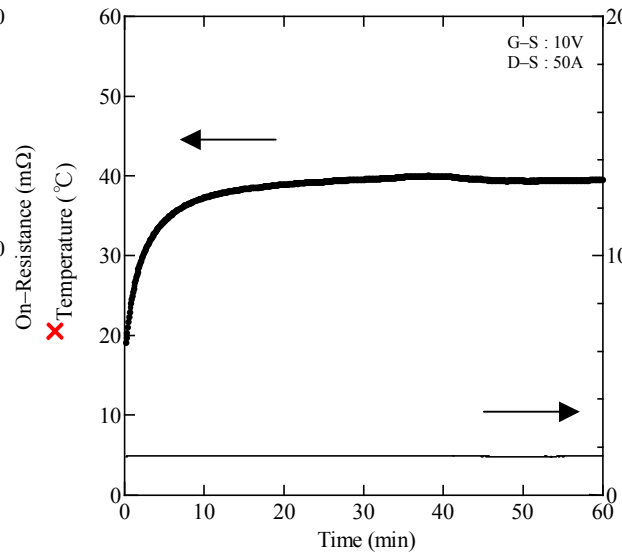
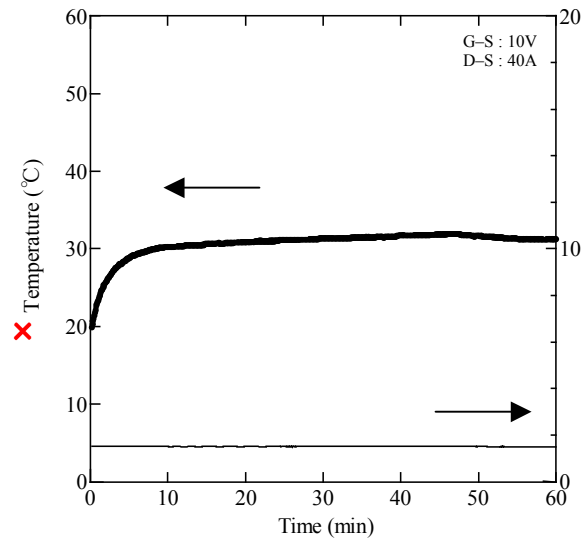
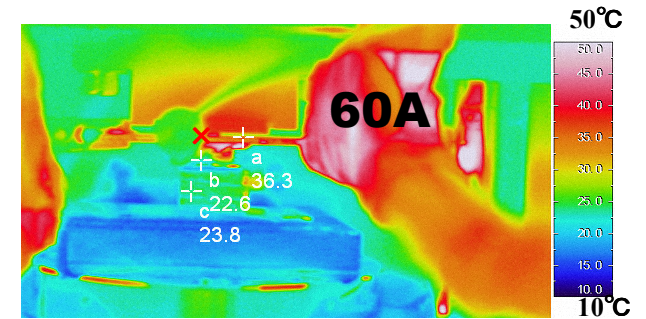
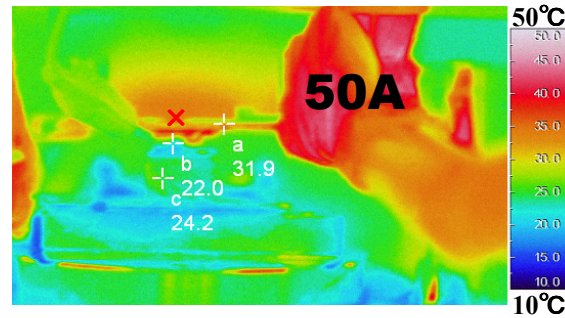
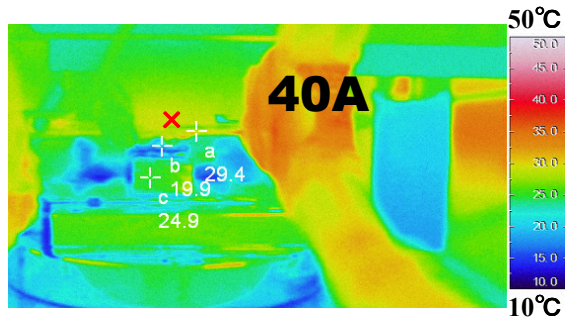
$$V_{GS} = 10\text{V}$$

$$I_{DS} = 40\text{A}, 50\text{A}, 60\text{A}$$



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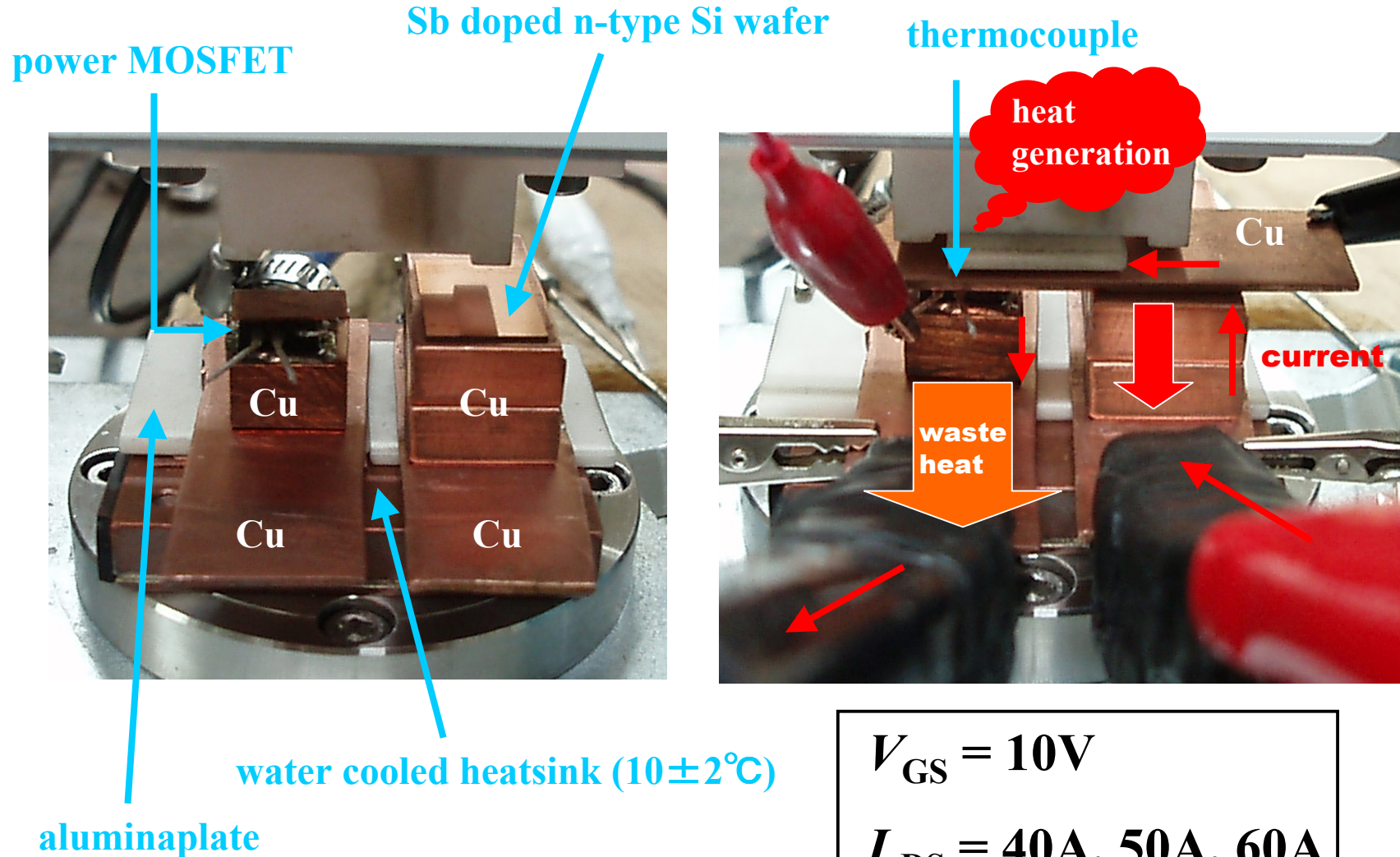
temperature survey 1





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self-cooling device 1

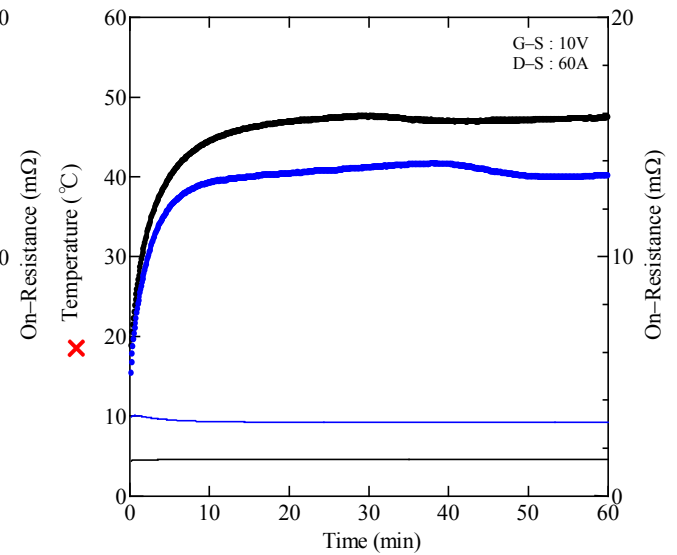
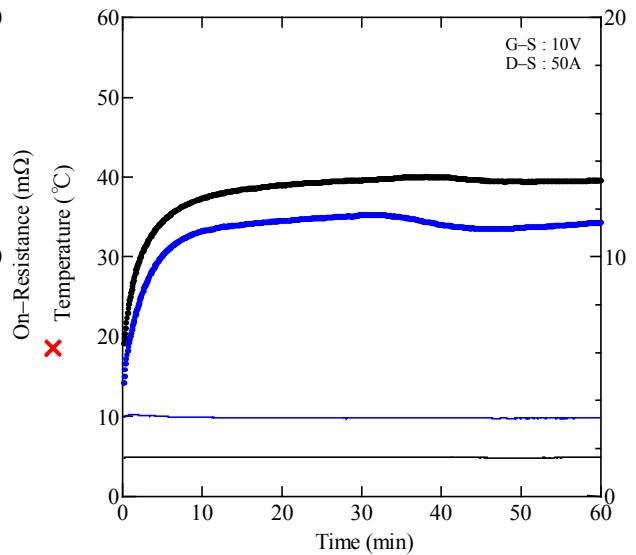
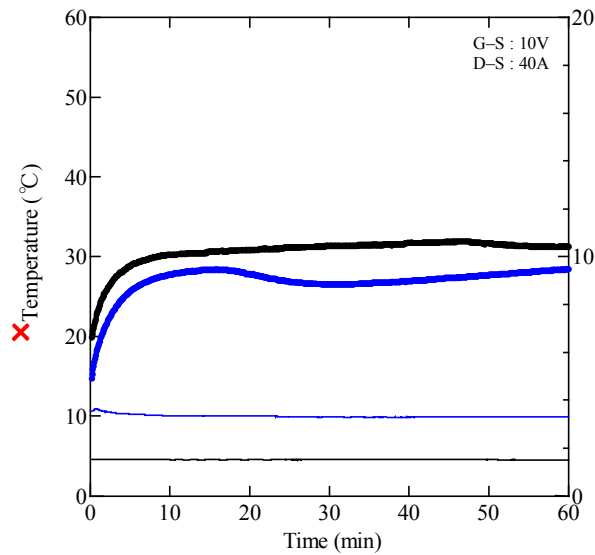
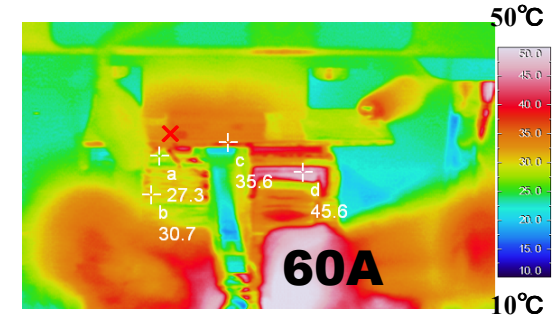
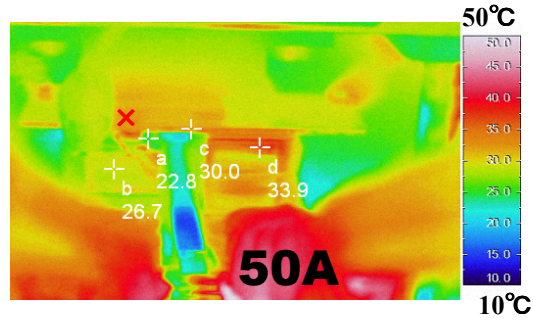
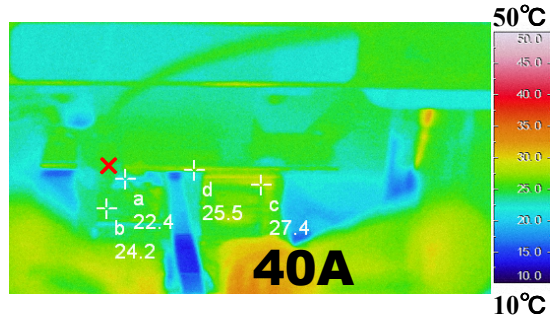


$$V_{GS} = 10V$$
$$I_{DS} = 40A, 50A, 60A$$



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temperature survey 2

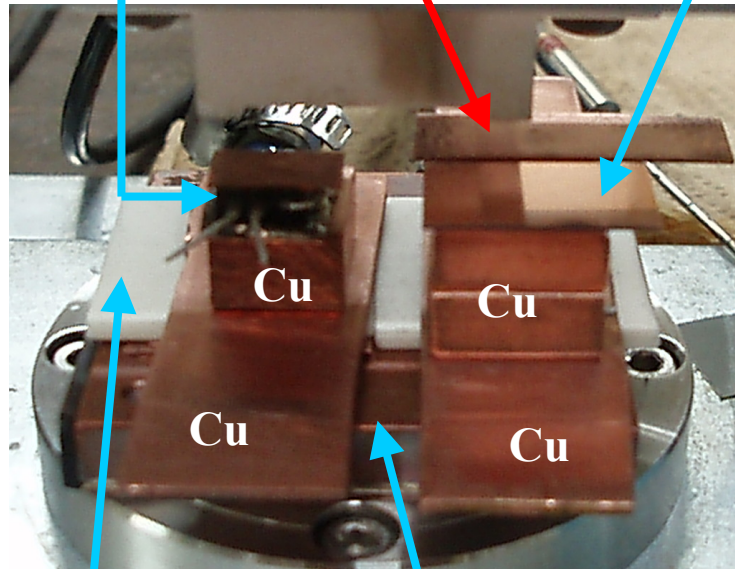




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self-cooling device 2

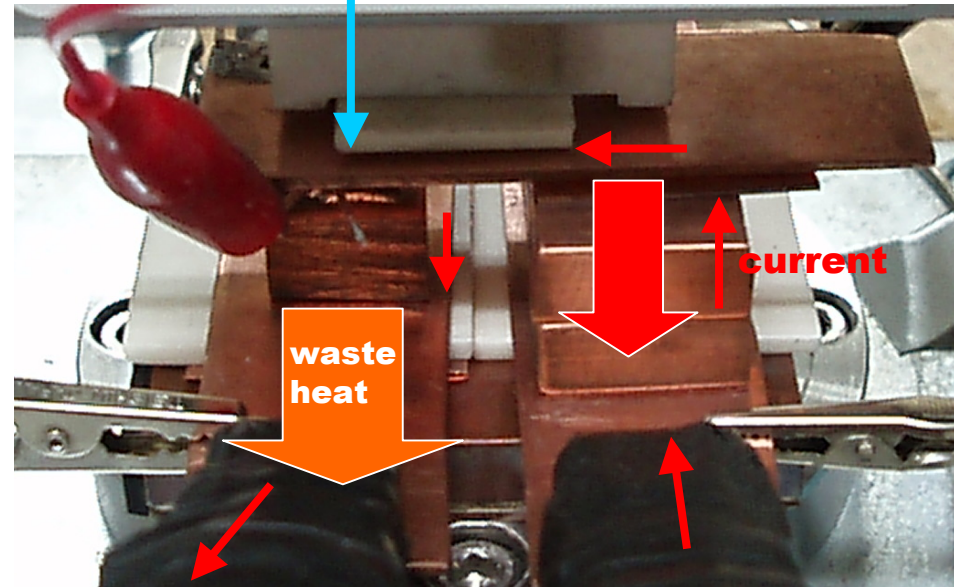
power MOSFET
copperplate
Sb doped n-type Si wafer



aluminaplate

water cooled heatsink ($10 \pm 2^\circ\text{C}$)

thermocouple



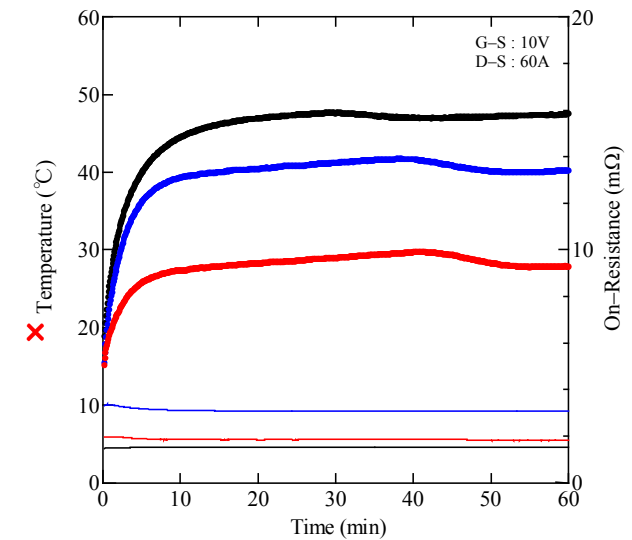
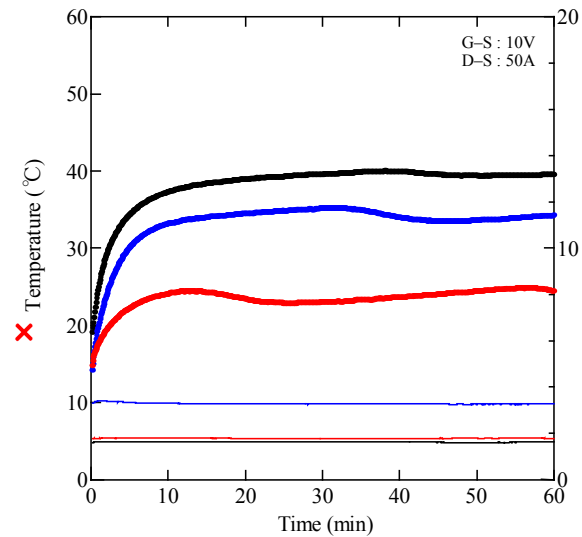
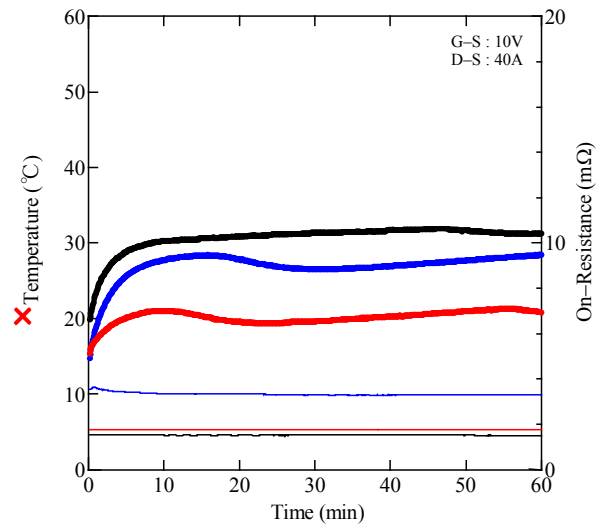
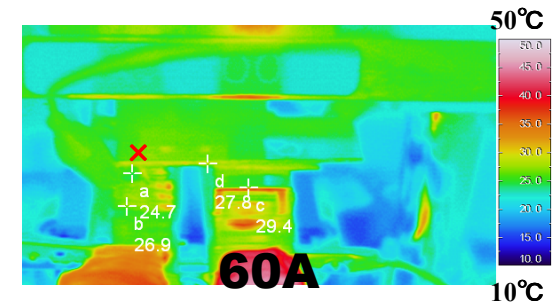
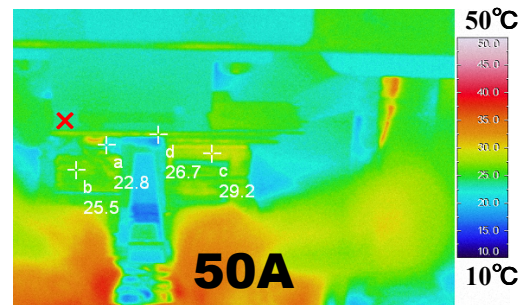
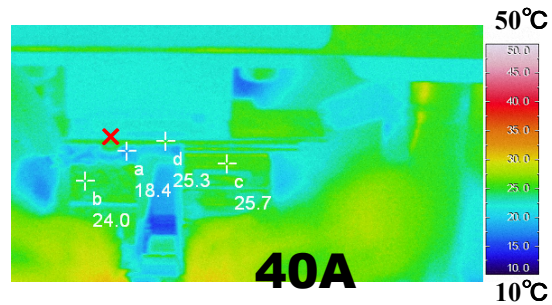
$$V_{GS} = 10\text{V}$$

$$I_{DS} = 40\text{A}, 50\text{A}, 60\text{A}$$



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temperature survey 3



Summary

- **The Sb doped n-type silicon (111) wafer has been applied to the self-cooling device.**
- **The self-cooling device using the heat flux both by Peltier effect and by thermal conduction has removed the heat generation on the upper side of the power MOSFET.**
- **In particular, the heat removal has been enhanced drastically by the increase of the heat flux.**

Acknowledgement

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Thank you for your attention.